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September 9, 2003

Attorney Docket No.: 07977-152003

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Presented for filing is a new divisional patent application of:

Applicant: HISASHI OHTANI

Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION
METHOD THEREOF

The prior application is assigned of record to Semiconductor Energy Laboratory Co., Ltd., a Japan corporation, by virtue of an assignment submitted to the Patent and Trademark Office and recorded on May 23, 1997 at 8584/0778.

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	22
Claims	7
Abstract	1
Declaration	2
Drawing(s)	11

Enclosures:


- Serial number card (green).
- Preliminary amendment, 10 pages.
- Transmittal of formal drawings (1 page) and formal drawings (Figs. 1A-12D, 11 sheets).
- Change of correspondence address, 1 page.
- Information disclosure statement, 1 page.
- PTO-1449, 2 pages.
- Copy of transmittal of priority document (2 pages), which was filed in the parent application. Please enter in this application.
- Copy of notice of recordation of assignment document as received in parent (4 pages). Please enter in this application.
- Copy of associate power of attorney as filed in parent (1 page). Please enter in this application.

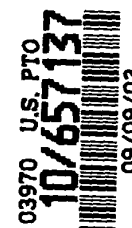
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This application is a divisional (and claims the benefit of priority under 35 USC 120) of U.S. application serial no. 09/967,697, filed September 28, 2001, which is a continuation of U.S. application serial no. 08/862,895, filed May 23, 1997, which, under 35 USC 119, claims the benefit of a foreign priority application filed in Japan as serial number 8-165272 filed on June 4, 1996. The disclosures of the prior applications are considered part of (and are incorporated by reference in) the disclosure of this application.

Basic filing fee	\$750
Total claims in excess of 20 times \$18	\$72
Independent claims in excess of 3 times \$84	\$420
Fee for multiple dependent claims	\$0
Total filing fee:	\$1242

A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

26171

PTO Customer Number

Respectfully submitted,



John F. Hayden
Reg. No. 37,640

Enclosures

JFH/aks

40175388.doc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani
Serial No. : New Divisional Application
Filed : September 9, 2003

Art Unit : Unknown
Examiner : Unknown

Title : SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION METHOD
THEREOF

Attention: Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed eleven (11) sheets of formal drawings (Figs. 1A-12D) for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 9, 2003

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CHANGE OF CORRESPONDENCE ADDRESS Application Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	Application Number	New Divisional Application
	Filing Date	September 9, 2003
	First Named Inventor	Hisashi Ohtani
	Group Art Unit	
	Examiner Name	
	Attorney Docket Number	07977-152003

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I am the :

- ☐ Applicant/Inventor.
- ☐ Assignee of record of the entire interest.
Certificate under 37 CFR 3.73(b) is enclosed.
- ☒ Attorney or agent of record. Registration Number 37,640
- ☐ Registered practitioner named in the application transmittal letter in an application without an executed oath or declaration. See 37 CFR 1.33(a)(1). Registration Number 37,640

Typed or Printed

Name John F. Hayden

Signature



Date September 9, 2003

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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.

☒ *Total of 1 forms are submitted.